



PATENT
Docket No. INTEL/17226

**IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE**

Applicants: **NANJA et al.**

U.S. Serial No.: 10/659,457

For: "Methods and Apparatus for Dynamic Best Fit Compilation of Mixed Mode Instructions"

Filed: September 10, 2003

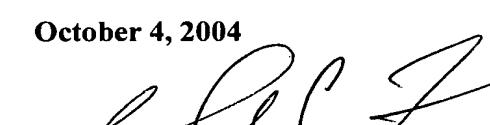
Assignee: Intel Corporation

Group Art Unit: 2122

Examiner: Not Yet Assigned

) I hereby certify that this paper and the documents referred to as enclosed therewith are being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on this date:

) **October 4, 2004**

) 

) **Mark C. Zimmerman**
Attorney for Applicant(s)
Registration No. 44,006

STATUS LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Kindly advise when an Office action can be expected in the above-referenced matter

Respectfully submitted,

GROSSMAN & FLIGHT, LLC
20 North Wacker Drive
Suite 4220
Chicago, Illinois 60606
(312) 580-1020

October 4, 2004

Bv:

Mark C. Zimmerman
Registration No.: 44,006

Attorneys for Intel Corporation